



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Takeshi KIJIMA et al.

Application No.: 10/805,238

Docket No.: 119169

Filed: March 22, 2004

For: ELECTRODE, METHOD OF MANUFACTURING THE SAME, FERROELECTRIC
MEMORY, AND SEMICONDUCTOR DEVICE

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

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Date: August 2, 2004

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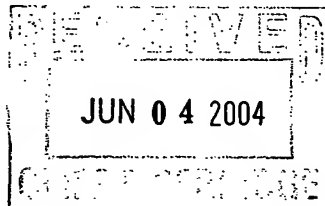


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/805,238	03/22/2004	2824	770	119169	21	12	2

25944
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CONFIRMATION NO. 9039

FILING RECEIPT



OC000000012858135

Date Mailed: 06/03/2004

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant

Foreign Applications

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 JAPAN 2003-275928 07/17/2003

If Required, Foreign Filing License Granted: 06/02/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Electrode, method of manufacturing the same, ferroelectric memory, and semiconductor device

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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